



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of:
Tomoe YAMAMOTO

Appl. No.: 09/450,351

Filed: November 29, 1999

For: METHOD FOR MANUFACTURING A
SEMICONDUCTOR DEVICE

Art Unit: 2812

Examiner: TSAI, H.

Atty Docket: SHM-00901

PATENT
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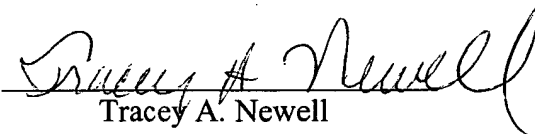
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TC 2800 MAIL ROOM

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CERTIFICATE OF MAILING

I hereby certify that the foregoing document is being deposited with the United States Postal Service as first class mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner of Patents, Washington, DC 20231 on December 11, 2001.


Tracey A. Newell

SUPPLEMENTAL RESPONSE

Commissioner for Patents
Washington, D.C. 20231

Sir:

This paper is being provided as a supplement to Applicant's November 21, 2001 response to the Office Action dated August 28, 2001, for the above-captioned U.S. patent application.

It is not believed that extensions of time or fees for net addition of claims are required, beyond those which may otherwise be provided for in documents accompanying this paper. However, in the event that additional extensions of time are necessary, then such extensions of time are hereby petitioned under 37 C.F.R. § 1.136(a), and any fees required for consideration of this paper (including fees for net addition of

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